IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Sehat Sutardja

Art Unit :
Examiner :

Unknown Unknown

Serial No.: Filed :

to be assigned

: HEREWITH

Title :

IMPROVED INTEGRATED CHIP PACKAGE HAVING

INTERMEDIATE SUBSTRATE

BOX PATENT APPLICATION

Commissioner for Patents Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449, copies of which are enclosed.

This statement is being filed with the application. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 9/27/01

Joseph L. Stevenson

Reg. No. 43,163

ERIC B. JANOFSY
Marvel Semiconductor, Inc.
645 Almanor Ave.
Sunnyvale, CO 94086

10128924.doc

CERTIFICATE OF MAILING BY EXPRESS MAIL

Express Mail Label No. __EL870690834US

I hereby certify under 37 CFR \$1.10 that this correspondence is being deposited with the United States Postal Service as Express Mail Post Office to Addressee with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

September 27, 2001
Date of Deposit

Signature

Michael Hubbard

Typed or Printed Name of Person Signing Certificate